

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. .... 09/517,127  
Filing Date ..... March 12, 2000  
Inventor ..... Scott E. Moore et al.  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 3724  
Examiner ..... T. Eley  
Attorney's Docket No. .... MI22-1246  
Title: Semiconductor Processor Systems, A System Configured to Provide a Semiconductor Workpiece Process Fluid

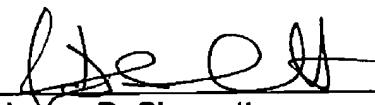
**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

References – See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with 37 CFR §1.56. No admission is made regarding whether all the submitted references are prior art.

Respectfully submitted,

Dated: 1/9/06

By:   
James D. Shaurette  
Reg. No. 39,833

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Sheet 1 of 1

Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. MI22-1246	SERIAL NO. 09/517,127		
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Scott E. Moore et al.			
				FILING DATE March 12, 2000	GROUP 3724		
U.S. PATENT DOCUMENTS							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	5,446,531	8/29/1995	Boyer et al.			
	AB	6,319,469B1	11/20/2001	Mian et al.			
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation
	AM						Yes No
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
EXAMINER			DATE CONSIDERED				
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>							

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